

Rajeev Institute of Technology

**Plot # 1-D (P-1), Growth Center, Industrial Area,
Bangalore-Mangalore Bypass Road, Hassan-573201.**

Chief Patrons

**Dr. V Rajeev, President,
Dr. B N Rathna, Secretary,
Rajeev Education Trust,
Hassan**

Patron

**Dr. A N Ramakrishna
Principal**

Chairperson

**Dr. Dhananjaya D A
Vice- Principal & HOD,
Mechanical Dept**

Chief Co-ordinator

**Dr. Aravind B N
HOD, E&CE**

Event Co-ordinator

**Mrs. Ambika K
Asst. Professor, E&CE**

Greetings from Department of Electronics and Communication Engineering

Dear Sir/Madam,

We are delighted to inform you that the technical club of Department of Electronics and Communication Engineering "**ELECTRORIT**" is organizing a two day workshop on "**Circuit Simulation and PCB Layout Design using ORCAD**" in association with **Entuple Technologies** on 11th and 12th February 2019. This workshop is intended to provide an insight in the field of circuit simulation and PCB layout generation for the second year students of E&CE.

We cordially invite the HOD's and Faculty members of Rajeev Institute of Technology to join the inaugural function that will be held on 11th February at Seminar Hall, 4th Floor at 9.30am.

With Warm Regards,

HOD & Faculty Members, Dept. of E&CE

Report on Two day Workshop on
“Circuit Simulation and PCB Layout Design Using ORCAD”
Organized by the Department of Electronics and Communication
Engineering, Rajeev Institute of Technology, Hassan on 11th and 12th
February 2019

Two day workshop on *“Circuit Simulation and PCB Layout Design using ORCAD”* was organized by the Department of Electronics and Communication Engineering, Rajeev Institute of Technology, Hassan on 11th and 12th of February 2019 for 4th semester students. 69 students were actively participated in the workshop.

The workshop was organized under the objective of “learn beyond the syllabus”. Here, a software based simulation using ORCAD was introduced. It involved both analog and digital circuit simulations. It was also extended upto the generation of PCB layout and its verification. The workshop topics aimed towards the easiness and self-understanding of circuits.

The workshop was conducted in technical association with Entuple Technologies India Ltd., **Mr. Kamlesh Kumar**, Application Engineer, Entuple technologies, Bangalore, was the resource person.

The inaugural ceremony witnessed the presence of **Dr. A N Ramakrishna**, Principal, RIT, Hassan, **Dr. Dananjaya D A**, Vice Principal and HOD, Department of Mechanical Engineering, RIT, Hassan, **Dr. Aravind B N**, Professor & HOD, Department of Electronics and Communication Engineering, RIT, Hassan, the HODs of various departments, teaching and non-teaching faculties.

Dr. A N Ramakrishna, Principal, RIT inaugurated the workshop traditionally by lighting the lamp. In the motivational talk, he emphasized upon the importance of learning new technologies beyond syllabus. He also highlighted the importance of this workshop and encouraged to conduct similar workshop in future too.

Dr. Dananjaya D A, Vice Principal and HOD, Department of Mechanical Engineering, highlighted the prominence of conducting workshops and inspired students to learn technologies from such workshops. He also highlighted scope of the ORCAD the vast for laboratory and project designs.

Department of Electronics and Communication Engineering, RIT, Hassan

Dr. Aravind B N, Professor and HOD, Department of electronics and Communication Engineering, welcomed the dignitaries, guests, faculties and participants. **Mrs. Ambika K**, Assistant Professor, Department of Electronics and communication Engineering presented the Vote of thanks.

Two day Workshop on "Circuit Simulation and PCB Layout Design Using ORCAD"

Overview

Working with analog and digital circuits in the laboratory as well as in professional level is a part and parcel of an electronics engineer. It is important to learn the way circuits are drawn, tested and finally the related PCB layouts are made. This is helpful in self-learning and also in the mini/major-project project designs at UG/PG level.

Course Objective:

- To introduce the design flow of a circuit.
- To draw and simulate basic circuits using ORCAD
- To provide participants with the knowledge to simulate and generate their own PCB layouts.

Learning Outcome:

- Usage of software for circuit representation.
- Using one environment to simulate and verify analog and digital circuits
- Confidence to make simulation of any circuits and generate the related PCB layouts.

Time	Topic	Comments
Day 1 (11-02-2019)		
Morning session		
9:30am-10:30am	Inaguration	
10:30am-10:45am	Tea break	
10:45am-1:00pm	Introduction to Design flow	Overview of ORCAD and hands-on session
	Introduction to ORCAD software & its capabilities	
	Design and simulation of rectifiers	
1:30pm-2:15pm	Lunch break	
Afternoon Session		
2:15pm-3:30pm	Clippers, Clampers	Practical sessions
	RF Amplifiers	
3:30pm-3:45pm	Tea Break	
3:45pm-5:00pm	Astable Multivibrator	
	Monostable Multivibrator	
Day 2 (12-02-2019)		
Morning session		

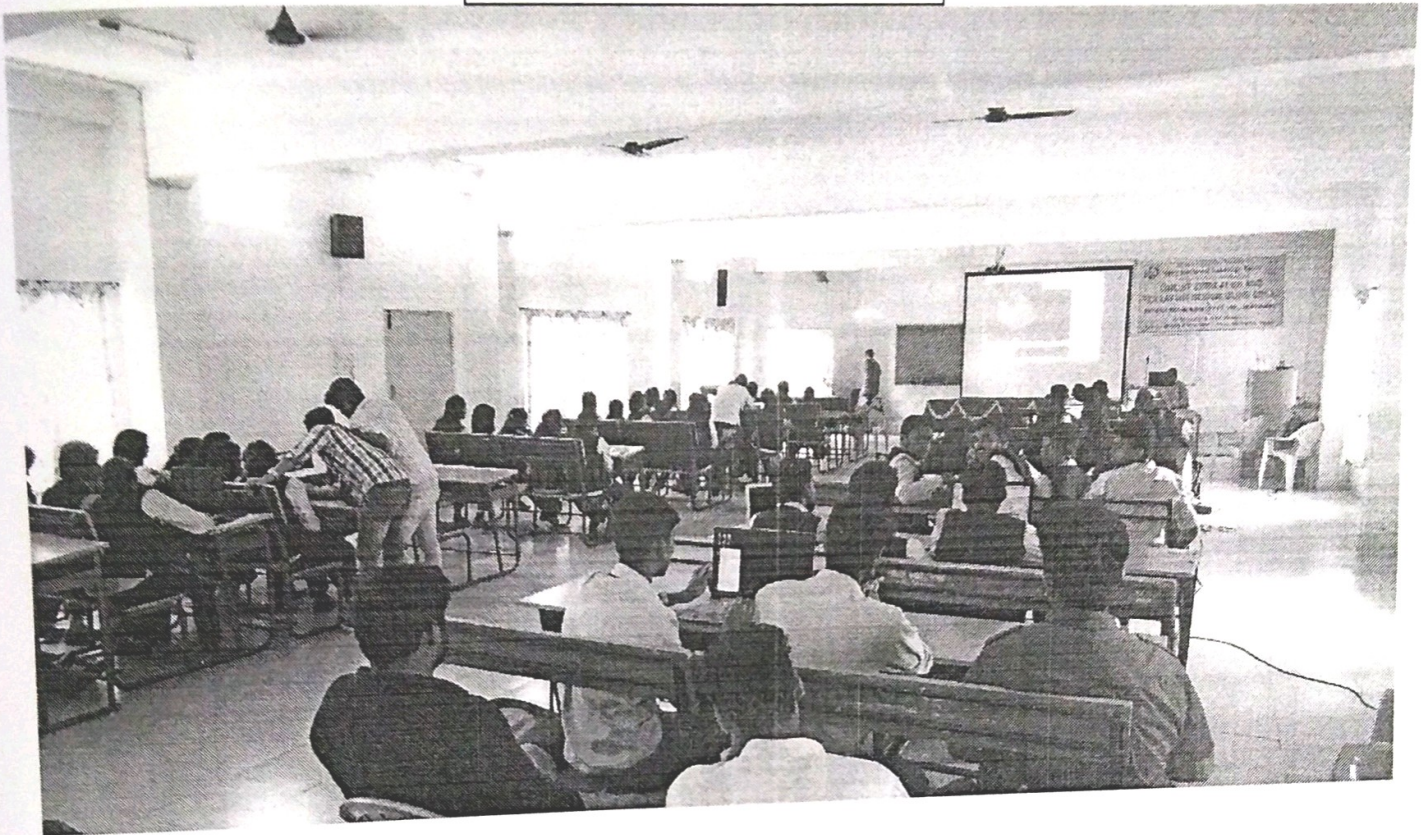
9:30am-11:15am	Digital Design: Basic gates	Practical sessions
	Half/full adder	
11:15am-11:30am	<i>Tea break</i>	
11:30am-1pm	Half/Full subtractor	Practical sessions
	Counter design	
1pm-2:15pm	<i>Lunch break</i>	
Afternoon session		
2:15pm-4pm	PCB Layout design for Half/full adder, Half/full subtractor	Practical sessions
4pm-4:30pm	Valedictory & High-Tea	

We thank the resource person Mr. Kamlesh Kumar, Application Engineer, Entuple Technologies India Ltd, Bangalore for providing a hands-on knowledge on analog, digital circuit simulation and also PCB layout generation.

Inauguration



Hands-on sessions



Department of Electronics and Communication Engineering, RIT, Hassan

Group photo during valedictory

